Product Features:

- SPI [Serial Peripheral Interface] Compatible Bus
  - SPI(x1) / SDI(x2) / SQI(x4) mode
- Very Low Bus Signal Count:
  - 6 pins for x4 IO : CS#, SCK, SIO0 - SIO3
  - 4 pins for x1/x2 IO : CS#, SCK, SI(SIO0), SO(SIO1)
- Very Simple Commands: 7 commands
  - Read/Write Memory
  - Read /Write Register
  - Enter SDI (x2)/SQI mode(x4), Return to SPI mode (x1)
- Max. Frequency*:
  - 2.2~3.6 V: LP:  20Mhz  HS:  45MHz
  - 1.65~2.2V: LP:  16Mhz  HS:  30MHz
- Ultra Low Power Consumption:
  - Read Current at 20MHz, 3.6V = 8 mA [Max]
  - Standby Current = 4µA [Typ]

Key Benefits of Serial SRAM:

1. Low Signal Pin Count & Smaller Footprint: 4 vs 30
   - Minimum 4 Signal Pins for 4Mb [x1]: SI, SO, CK, CS#
   - 30 signals for 4Mb SRAM [x8] : 19 Address, 8 IOs, 3 Controls
2. Simple SPI Compatible Bus & LVCMOS Compatible Input
   - Easy to use on the existing interface - FPGA, MCU
3. Random Read/Write Operation with Ultra Low Standby Current
   - SRAM Performance with much smaller Footprint, with 4µA Standby Current

Applications:
- IoT
- Advanced Driver Assistance Systems
- Industrial Application
- Medical

Package:
- KGD/KTD
- 8-pin SOIC [150 mil]