### ISSI DDR4 Product Features:

- **Standard Voltage:** VDD = VDDQ = 1.2V, VPP = 2.5V
- **Data Integrity**
  - Auto Self Refresh (ASR) by DRAM built-in
  - Auto Refresh and Self Refresh Modes
- **DRAM access bandwidth**
  - Separated IO gating structures by Bank Group
  - Self Refresh Abort
  - Fine Granularity Refresh
- **Signal Synchronization**
  - Write Leveling via MR settings
  - Read Leveling via MPR
- **Reliability & Error Handling**
  - Command/Address Parity
  - Data bus Write CRC
  - MPR readout
  - Boundary Scan [x16]
- **Signal Integrity**
  - Internal VREF-DQ Training
  - Read Preamble Training
  - Gear Down Mode
  - Per DRAM Addressability
  - Configurable DS for system compatibility
  - Configurable On-Die Termination
  - Data bus Inversion (DBI)
  - ZQ Calibration for DS/ODT impedance accuracy via external ZQ pad (240 ohm +/- 1%)
- **Power Saving and efficiency**
  - POD with VDDQ termination
  - Command/Address Latency (CAL)
  - Maximum Power Saving
  - Low power Auto Self Refresh (LPASR)
- **Operating Temperature**
  - Commercial [Tc = 0C to + 95C]
  - Industrial [Tc = -40C to + 95C]
  - Automotive, A1 [Tc = -40C to + 95C]
  - Automotive, A2 [Tc = -40C to + 105C]
  - Automotive, A3 [Tc = -40C to + 125C]

### ISSI DDR4 SDRAM Ordering Options

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</tr>
</thead>
<tbody>
<tr>
<td>4G</td>
<td>2666,2400</td>
<td>IS43QR85120B</td>
<td>512Mx8</td>
<td>BGA[78]</td>
<td>Prod</td>
</tr>
<tr>
<td></td>
<td>2666,2400</td>
<td>IS43QR16256B</td>
<td>256Mx16</td>
<td>BGA[96]</td>
<td>Prod</td>
</tr>
<tr>
<td>8G</td>
<td>2666,2400</td>
<td>IS43QR81024A</td>
<td>1Gx8</td>
<td>BGA[78]</td>
<td>Prod</td>
</tr>
<tr>
<td></td>
<td>2666,2400</td>
<td>IS43QR16512A</td>
<td>512Mx16</td>
<td>BGA[96]</td>
<td>Prod</td>
</tr>
<tr>
<td>16G</td>
<td>2400</td>
<td>IS43QR8K02S2A</td>
<td>2Gx8</td>
<td>BGA[78]</td>
<td>Prod</td>
</tr>
</tbody>
</table>

### ISSI DDR4 SDRAM Automotive Ordering Options

<table>
<thead>
<tr>
<th></th>
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<th></th>
</tr>
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<tr>
<td>4G</td>
<td>2666,2400</td>
<td>IS46QR85120B</td>
<td>512Mx8</td>
<td>BGA[78]</td>
<td>Prod</td>
</tr>
<tr>
<td></td>
<td>2666,2400</td>
<td>IS46QR16256B</td>
<td>256Mx16</td>
<td>BGA[96]</td>
<td>Prod</td>
</tr>
<tr>
<td>8G</td>
<td>2666,2400</td>
<td>IS46QR81024A</td>
<td>1Gx8</td>
<td>BGA[78]</td>
<td>Prod</td>
</tr>
<tr>
<td></td>
<td>2666,2400</td>
<td>IS46QR16512A</td>
<td>512Mx16</td>
<td>BGA[96]</td>
<td>Prod</td>
</tr>
<tr>
<td>16G</td>
<td>2400</td>
<td>IS46QR8K02S2A</td>
<td>2Gx8</td>
<td>BGA[78]</td>
<td>Prod</td>
</tr>
</tbody>
</table>