ISSI introduces its new Family of IS25LP [2.5/3V] and IS25WP/WJ [1.8V] series of flash devices. The family builds upon the success of ISSI’s IS25LQ [2.5/3V] and IS25WQ [1.8V] family by introducing leading edge features such as double data rate* [DTR/DDR] interface modes, SFDP support, and the popular 2 cycle instruction input [QPI mode].

The new product family offers read speeds up to 166Mhz in Single/Dual/Quad I/O and 80 MHz in double data rate [DTR/DDR] modes, delivering up to 664Mb/s (equivalent 83MB/sec) of read throughput.

ISSI’s SPI NOR Flash are ideal for a broad range of applications, such as Automotive, Industrial, Medical, Communications, Networking, Smart Meters, FPGA, Digital Cameras, Printers, Bluetooth, and IOT. The family is also ideal for code shadowing, execute in place [XIP], and data storage operations.

**Industry Standard Serial Interface**
- IS25LP[WP]02GG/GJ: 2Gb/256MB
- IS25LP[WP]01GG/GJ: 1Gb/128MB
- IS25LP[WP]512MG/MJ: 512Mb/64MB
- IS25LP[WP]256D: 256Mb/32MB
- IS25LP[WP/WJ]064D/F: 64Mb/8MB
- IS25LP[WP/WJ]032D/F: 32Mb/4MB
- IS25LP[WP]016D: 16Mb/2MB
- IS25LP[WP]080D: 8Mb/1MB
- IS25LP040E/IS25WP040E: 4Mb/512KB
- IS25LP020E/IS25WP020E: 2Mb/256KB
- IS25LP010E: 1Mb/128KB
- IS25LP512E: 512Kb/64KB

**High Performance Serial Flash (LP/WP Series)**
- 50MHz Normal and up to 166Mhz Fast Read
- 664 MHz equivalent QPI SPI
- DTR [Dual Transfer Rate] up to 80MHz
- Selectable dummy cycles
- Configurable drive strength
- Supports SPI Modes 0 and 3
- More than 100,000 erase/program cycles
- More than 20-year data retention

**Flexible & Efficient Memory Architecture**
- Chip Erase: Uniform: Sector and Block Erase [4K/32K/64K-Byte]
- Program 1 to 256 bytes per page
- Program/Erase Suspend & Resume

**Efficient Read and Program Modes**
- Low Instruction Overhead Operations
- Continuous Read 8/16/32/64-Byte Wrap
- Selectable burst length
- QPI for reduced instruction overhead
- Allows XIP operations [execute in place]

**Low Power with Wide Temp. Ranges**
- Single Voltage Supply
- Low Standby Current
- Deep Power Down mode
- Temperature Grades:
  - Extended: -40°C to +105°C
  - Auto Grades: up to +125°C

*Double data rate [DTR/DDR] available on WP/LP family over 4Mbit densities.*
**Advanced Security Protection**
- Software and Hardware Write Protection
- Power Supply lock protect
- 4x256-Byte dedicated security area with user-lockable bits, (OTP) One Time Programmable Memory
- 128 bit Unique ID for each device (Call Factory)

### Serial Flash Clock Rate & Data Throughput

<table>
<thead>
<tr>
<th>SPI Protocols</th>
<th>256K</th>
<th>512K</th>
<th>1M</th>
<th>2M</th>
<th>4M</th>
<th>8M</th>
<th>16M</th>
<th>32M</th>
<th>64M</th>
<th>128M</th>
<th>256M</th>
<th>512M</th>
<th>1G</th>
<th>2G</th>
</tr>
</thead>
<tbody>
<tr>
<td>Single I/O</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>104</td>
<td>104</td>
<td>104</td>
<td>104</td>
<td>104</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>104</td>
<td>104</td>
<td>104</td>
<td>104</td>
<td>133</td>
</tr>
<tr>
<td>Dual I/O</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>208</td>
<td>208</td>
<td>208</td>
<td>208</td>
<td>208</td>
</tr>
<tr>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>208</td>
<td>208</td>
<td>208</td>
<td>208</td>
<td>266</td>
</tr>
<tr>
<td>Quad I/O</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>266</td>
<td>266</td>
<td>266</td>
<td>266</td>
<td>266</td>
</tr>
<tr>
<td>Quad I/O with Double Transfer Rate</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td>66</td>
<td>66</td>
<td>66</td>
<td>66</td>
<td>66</td>
</tr>
<tr>
<td>Throughput</td>
<td>Unit: Mb/s</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
<tr>
<td>Clock Rate</td>
<td>Unit: MHz</td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
<td></td>
</tr>
</tbody>
</table>

June 2024 • 1623 Buckeye Dr. Milpitas, CA 95035 • Tel: 408-969-6600 • Support: flash@issi.com • www.issi.com