



INTEGRATED SILICON SOLUTION, INC.

ISSI Introduces Industry's Highest-Performance, Wide Voltage, Quad-Interface Dual Transfer Rate (DTR) Serial NOR Flash Memory

Targeted for Automotive, Industrial, Medical, Communications, Networking, Smart Meters, FPGA, Digital Still Cameras, Printers, Bluetooth and IOT

MILPITAS, Calif., May 13, 2014 (GLOBE NEWSWIRE) -- Integrated Silicon Solution, Inc. (Nasdaq:ISSI), a leader in advanced memory and analog solutions, introduces its new Family of IS25LP- series of Dual Transfer Rate (DTR) flash devices. The IS25LP family builds upon the success of ISSI's IS25LQ family by introducing leading edge features such as DTR interface, Serial Flash Discoverable Parameters (SFDP), and two cycle instruction input (QPI mode).

DTR mode enables an increased throughput by maintaining slower clock speeds, surpassing parallel NOR flash while maintaining a standard 8-pin package. With today's application trends needing higher throughput and support for greater bandwidth, SPI-DTR based NOR products ensure developers will be able to achieve end-product form factors that are smaller, consume lower power, and reduced system cost.

The IS25LP family offers read speeds up to 133MHz in Single/Dual/Quad I/O and 66 MHz in DTR modes, the IS25LP family delivers up to 532Mb/second (equivalent 66MB/sec) of read throughput and is faster than competitive solutions in the market. In addition, fast programming performance decreases manufacturing costs while lowering programming time dramatically. ISSI's IS25LP family also offers the fastest erase time of 78KB/s in the industry, almost doubling the competition in this class of SPI-DTR NOR flash.

ISSI's high-performance, reduced pin count, IS25LP family is ideal for a broad range of applications, such as automotive, industrial, medical, communications, networking, smart meters, FPGA, digital still cameras, printers, Bluetooth and IOT. The family currently offers 128Mb, 64Mb, and 32Mb densities, with 256Mb in the near future.

"There is a growing demand for a reduced pin count, wide voltage, high-performance devices with enhanced features and functionality added," said Manjunatha Kashi, VP Business Development Flash at ISSI.

ISSI's IS25LP family offers high densities that are ideal for code shadowing, execute in place (XIP) and data storage. The IS25LP family of serial flash is fully backwards compatible for systems requiring a density migration path.

The IS25LP family is loaded with enhanced super-set features compared to a standard serial flash. The IS25LP products supports multiple interfaces such as standard SPI, Fast, Dual, Dual I/O, QPI, DTR, Dual DTR I/O, and QPI DTR, with SFDP. In-order to enhance the XIP feature, the dummy cycles are configurable. The other superset features are programmable drive strength, 4x256-Byte OTP area, and a factory programmable 128 bit Unique ID and flexibility of sector granularities of 4KB, 32KB, and 64KB.

Devices are currently sampling in 8-SOIC 208mil and 16-SOIC 300mil packages. Other packages include 8-VSOP 208mil, WSON 6x5mm, and WSON 8x6mm, will be available in Q3/14. All devices currently support Extended grade temperatures of -40 to 105°C with Automotive grade devices- up to 125°C in Q3/14. For KGD and WLCSP packages email: flash@issi.com.



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About ISSI

ISSI is a fabless semiconductor company that designs and markets high performance integrated circuits for the following key markets: (i) automotive, (ii) communications, (iii) industrial, medical, and military, and (iv) digital consumer. ISSI's primary products are high speed and low power SRAM and low and medium density DRAM. ISSI also designs and markets NOR flash products and high performance analog and mixed signal integrated circuits. ISSI is headquartered in Silicon Valley with worldwide offices in Taiwan, Japan, Singapore, China, Europe, Hong Kong, India, and Korea. Visit our website at <http://www.issi.com>.

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